

Altium Designer 19

설계파일 차이점 비교

2019.8.5

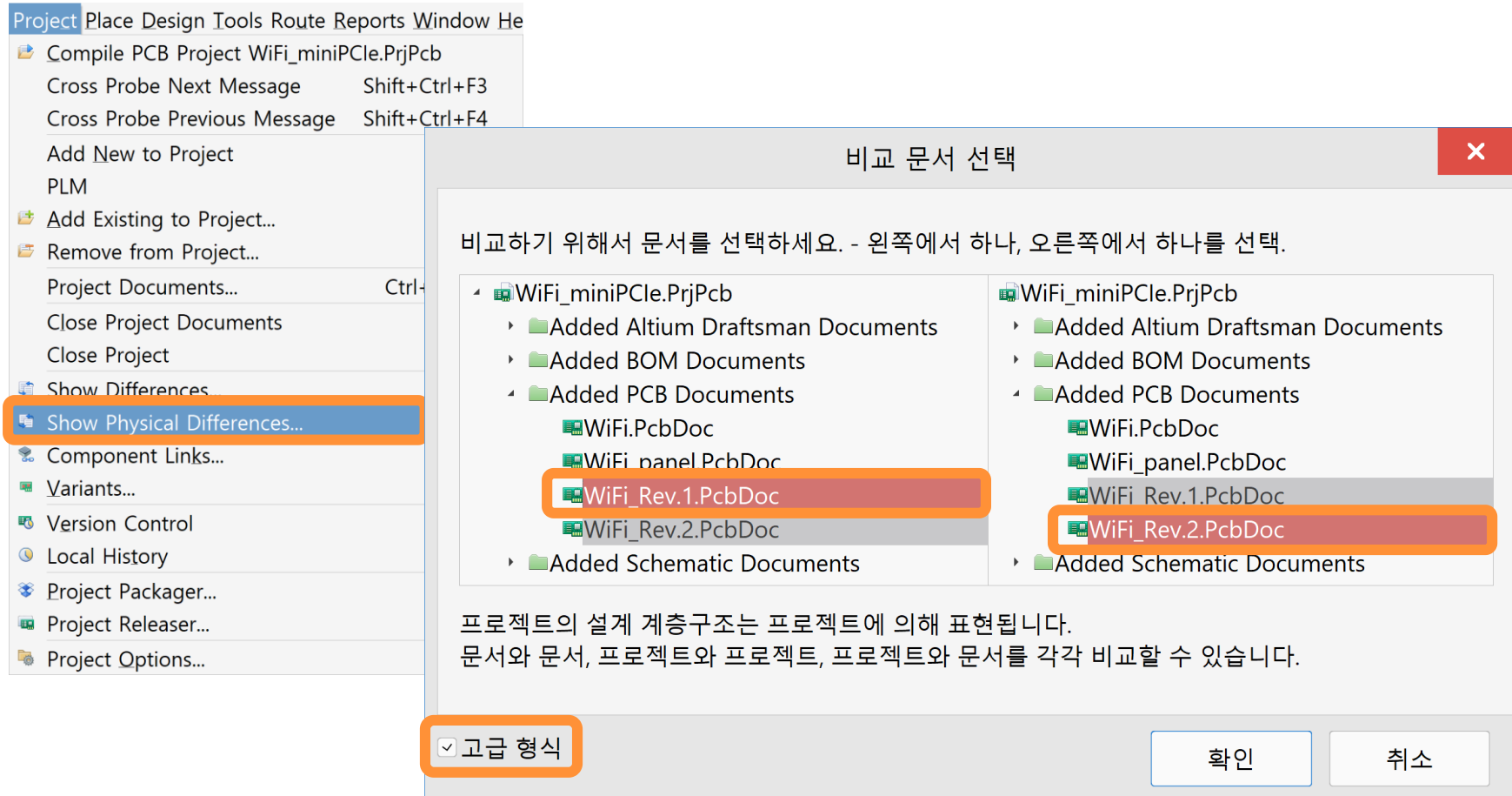
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1. 회로도/PCB 차이점 비교

설계파일 차이점 비교

- **Project » Show Physical Differences...** 를 실행하면 비교 분서 선택 창이 나타난다. 비교 문서 선택 창에서 고급 형식을 선택 후 비교할 두개의 파일을 왼쪽, 오른쪽 창에서 각각 선택하고 확인 버튼을 클릭한다.



설계파일 차이점 비교

- *. Show Physical Differences... 실행 후, Message 패널과 Differences 패널에서 두 비교대상의 차이점을 확인할 수 있다.

The screenshot displays two panels from the Altium Designer software interface:

- Messages Panel:** A table listing comparison messages. The columns are Class, Document, Source, Message, Time, Date, and No.
- Differences Panel:** A tree view showing detected differences, including a folder for 'Changed PCB Objects(30)' with a list of component differences.

Class	Document	Source	Message	Time	Date	No.
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Component C13 in WiFi_Rev.1.PcbDoc <=> Component C13 in WiFi_Rev.2.PcbDoc	오전 11:58:44	2019-04-23	14
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Component R30 in WiFi_Rev.1.PcbDoc <=> Component R30 in WiFi_Rev.2.PcbDoc	오전 11:58:44	2019-04-23	15
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Component DS3 in WiFi_Rev.1.PcbDoc <=> Component DS3 in WiFi_Rev.2.PcbDoc	오전 11:58:44	2019-04-23	16
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Component R12 in WiFi_Rev.1.PcbDoc <=> Component R12 in WiFi_Rev.2.PcbDoc	오전 11:58:44	2019-04-23	17

Differences Detected(30)

- Changed PCB Objects(30)
 - Component C10 in WiFi_Rev.1.PcbDoc <=> Component C10 in WiFi_Rev.2.PcbDoc
 - Component C11 in WiFi_Rev.1.PcbDoc <=> Component C11 in WiFi_Rev.2.PcbDoc
 - Component C12 in WiFi_Rev.1.PcbDoc <=> Component C12 in WiFi_Rev.2.PcbDoc
 - Component C13 in WiFi_Rev.1.PcbDoc <=> Component C13 in WiFi_Rev.2.PcbDoc
 - Component C14 in WiFi_Rev.1.PcbDoc <=> Component C14 in WiFi_Rev.2.PcbDoc
 - Component C19 in WiFi_Rev.1.PcbDoc <=> Component C19 in WiFi_Rev.2.PcbDoc
 - Component C25 in WiFi_Rev.1.PcbDoc <=> Component C25 in WiFi_Rev.2.PcbDoc
 - Component C37 in WiFi_Rev.1.PcbDoc <=> Component C37 in WiFi_Rev.2.PcbDoc
 - Component C38 in WiFi_Rev.1.PcbDoc <=> Component C38 in WiFi_Rev.2.PcbDoc
 - Component C39 in WiFi_Rev.1.PcbDoc <=> Component C39 in WiFi_Rev.2.PcbDoc
 - Component DS2 in WiFi_Rev.1.PcbDoc <=> Component DS2 in WiFi_Rev.2.PcbDoc
 - Component DS3 in WiFi_Rev.1.PcbDoc <=> Component DS3 in WiFi_Rev.2.PcbDoc
 - Component R9 in WiFi_Rev.1.PcbDoc <=> Component R9 in WiFi_Rev.2.PcbDoc
 - Component R11 in WiFi_Rev.1.PcbDoc <=> Component R11 in WiFi_Rev.2.PcbDoc
 - Component R12 in WiFi_Rev.1.PcbDoc <=> Component R12 in WiFi_Rev.2.PcbDoc

설계파일 차이점 비교

WiFi_miniPcb - Altium Designer (19.0.15)

File Edit View Project Place Design Tools Route Reports Window Help

WiFi_Rev.1.PcbDoc WiFi_Rev.2.PcbDoc

Projects Navigator PCB PCB Filter View Configuration PCB ActiveRoute Components Properties

LS [1] TopLayer [2] int1_power [3] int2_gnd [4] BottomLayer LS [1] TopLayer [2] int1_power [3] int2_gnd [4] BottomLayer C

Messages

Class	Document	Source	Message	Time	Date	No.
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Net PA3_USART2_RX in WiFi_Rev.1.PcbDoc <=> Net PA3_USART2_RX in WiFi_Rev.2.PcbDoc	오후 12:36:15	2019-04-23	11
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Net PA4_GPIO6_WIFI in WiFi_Rev.1.PcbDoc <=> Net PA4_GPIO6_WIFI in WiFi_Rev.2.PcbDoc	오후 12:36:15	2019-04-23	1

X:11.375mm Y:10.025mm Grid: 0.025mm (Hotspot Snap (All Layers)) 3D Extruded (Bottom 3D) Standoff=0.05mm Overall=1.6mm (116mm, 113.2) 3D Extruded (Bottom 3D) Standoff=0.05mm Overall= Panels

Idle state - ready for command

설계파일 차이점 비교

WiFi_miniPCle.PrjPcb - Altium Designer (19.0.15)

File Edit View Project Place Design Tools Route Reports Window Help

WiFi_Rev.1.PcbDoc WiFi_Rev.2.PcbDoc

Projects Navigator PCB PCB Filter View Configuration PCB ActiveRoute Components Properties

GND 비아 추가로 인한 폴리건 영역 변경

LS [1] TopLayer [2] int1_power [3] int2_gnd [4] BottomLayer LS [1] TopLayer [2] int1_power [3] int2_gnd [4] BottomLayer C

Messages

Class	Document	Source	Message	Time	Date	No.
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Net 3V3 in WiFi_Rev.1.PcbDoc <=> Net 3V3 in WiFi_Rev.2.PcbDoc	오후 12:36:15	2019-04-23	24
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Net GND in WiFi_Rev.1.PcbDoc <=> Net GND in WiFi_Rev.2.PcbDoc	오후 12:36:15	2019-04-23	30

X:9.95mm Y:28.075mm Grid: 0.025mm (Hotspot Snap (All Layers))

Idle state - ready for command

설계파일 차이점 비교

WiFi_miniPcb.PrjPcb - Altium Designer (19.0.15)

File Edit View Project Place Design Tools Route Reports Window Help

WiFi_Rev.1.PcbDoc WiFi_Rev.2.PcbDoc

Projects Navigator PCB PCB Filter View Configuration PCB ActiveRoute Components Properties

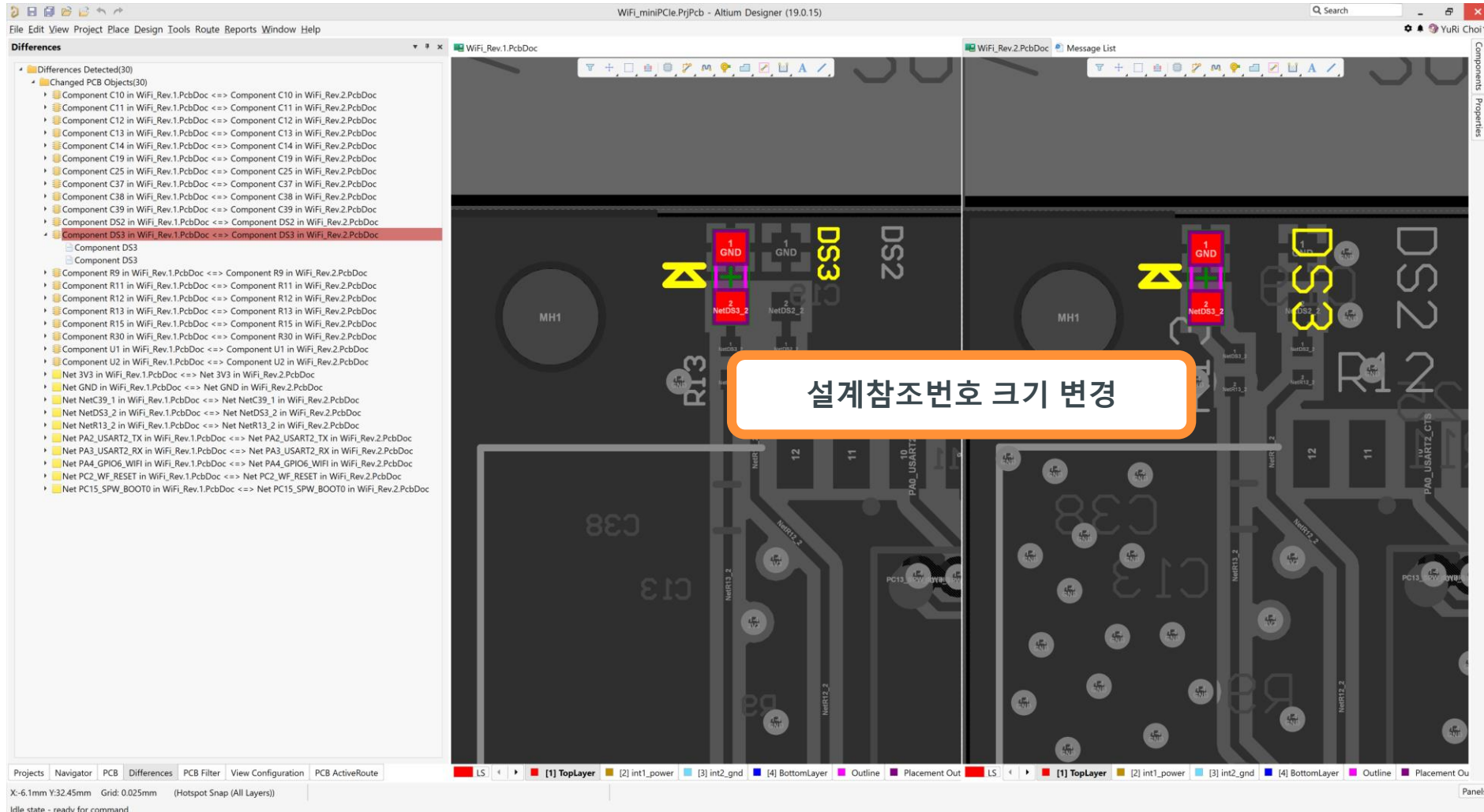
Messages

Class	Document	Source	Message	Time	Date	No.
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Component R15 in WiFi_Rev.1.PcbDoc <=> Component R15 in WiFi_Rev.2.PcbDoc	오후 12:36:15	2019-04-23	7
[Changed PCB Objects]	WiFi_Rev.1.PcbDoc	Comparator	Component R30 in WiFi_Rev.1.PcbDoc <=> Component R30 in WiFi_Rev.2.PcbDoc	오후 12:36:15	2019-04-23	15

X:10.2mm Y:11.45mm Grid: 0.025mm (Hotspot Snap (All Layers)) Track (10.325mm,10mm)(10.325mm,17.125mm) on TopLayer Track: (Net: NetR12_2 Width:0.254mm Length:7.125mm) Panels

Idle state - ready for command

설계파일 차이점 비교



설계파일 차이점 비교

The screenshot displays the Altium Designer interface for a project named 'WiFi_miniPCle.Pcb - Altium Designer (19.0.15)'. The 'Differences' panel on the left lists 30 detected differences between 'WiFi_Rev.1.PcbDoc' and 'WiFi_Rev.2.PcbDoc'. The list includes changes to various components (C10-C39, R9-R15, R30, U1, U2) and nets (Net 3V3, Net GND, Net NetC39_1, Net NetDS3_2, Net NetR13_2, Net PA2_USART2_TX, Net PA3_USART2_RX, Net PA4_GPIO6_WIFI, Net PC2_WF_RESET, Net PC15_SPW_BOOT0). The 'Net NetDS3_2' difference is highlighted in red.

The main workspace shows two side-by-side PCB layouts. A callout box with the text '부품 위치 변경' (Component Position Change) points to a component location change in the PCB design. The callout box is orange with white text.

At the bottom of the interface, there is a legend for the PCB layers: [1] TopLayer, [2] int1_power, [3] int2_gnd, [4] BottomLayer, Outline, Placement Out. The status bar at the bottom indicates 'Idle state - ready for command'.

설계파일 차이점 비교

Differences Detected(30)

- Changed PCB Objects(30)
 - Component C10 in WiFi_Rev.1.PcbDoc <=> Component C10 in WiFi_Rev.2.PcbDoc
 - Component C11 in WiFi_Rev.1.PcbDoc <=> Component C11 in WiFi_Rev.2.PcbDoc
 - Component C12 in WiFi_Rev.1.PcbDoc <=> Component C12 in WiFi_Rev.2.PcbDoc
 - Component C13 in WiFi_Rev.1.PcbDoc <=> Component C13 in WiFi_Rev.2.PcbDoc
 - Component C14 in WiFi_Rev.1.PcbDoc <=> Component C14 in WiFi_Rev.2.PcbDoc
 - Component C19 in WiFi_Rev.1.PcbDoc <=> Component C19 in WiFi_Rev.2.PcbDoc
 - Component C25 in WiFi_Rev.1.PcbDoc <=> Component C25 in WiFi_Rev.2.PcbDoc
 - Component C37 in WiFi_Rev.1.PcbDoc <=> Component C37 in WiFi_Rev.2.PcbDoc
 - Component C38 in WiFi_Rev.1.PcbDoc <=> Component C38 in WiFi_Rev.2.PcbDoc
 - Component C39 in WiFi_Rev.1.PcbDoc <=> Component C39 in WiFi_Rev.2.PcbDoc
 - Component D52 in WiFi_Rev.1.PcbDoc <=> Component D52 in WiFi_Rev.2.PcbDoc
 - Component D53 in WiFi_Rev.1.PcbDoc <=> Component D53 in WiFi_Rev.2.PcbDoc
 - Component R9 in WiFi_Rev.1.PcbDoc <=> Component R9 in WiFi_Rev.2.PcbDoc
 - Component R11 in WiFi_Rev.1.PcbDoc <=> Component R11 in WiFi_Rev.2.PcbDoc
 - Component R12 in WiFi_Rev.1.PcbDoc <=> Component R12 in WiFi_Rev.2.PcbDoc
 - Component R13 in WiFi_Rev.1.PcbDoc <=> Component R13 in WiFi_Rev.2.PcbDoc
 - Component R15 in WiFi_Rev.1.PcbDoc <=> Component R15 in WiFi_Rev.2.PcbDoc
 - Component R30 in WiFi_Rev.1.PcbDoc <=> Component R30 in WiFi_Rev.2.PcbDoc
 - Component U1 in WiFi_Rev.1.PcbDoc <=> Component U1 in WiFi_Rev.2.PcbDoc
 - Component U2 in WiFi_Rev.1.PcbDoc <=> Component U2 in WiFi_Rev.2.PcbDoc
 - Net 3V3 in WiFi_Rev.1.PcbDoc <=> Net 3V3 in WiFi_Rev.2.PcbDoc
 - Net GND in WiFi_Rev.1.PcbDoc <=> Net GND in WiFi_Rev.2.PcbDoc
 - Net NetC39_1 in WiFi_Rev.1.PcbDoc <=> Net NetC39_1 in WiFi_Rev.2.PcbDoc
 - Net NetD53_2 in WiFi_Rev.1.PcbDoc <=> Net NetD53_2 in WiFi_Rev.2.PcbDoc
 - Net NetR13_2 in WiFi_Rev.1.PcbDoc <=> Net NetR13_2 in WiFi_Rev.2.PcbDoc**
 - Net NetR13_2
 - Net NetR13_2
 - Net PA2_USART2_TX in WiFi_Rev.1.PcbDoc <=> Net PA2_USART2_TX in WiFi_Rev.2.PcbDoc
 - Net PA3_USART2_RX in WiFi_Rev.1.PcbDoc <=> Net PA3_USART2_RX in WiFi_Rev.2.PcbDoc
 - Net PA4_GPIO6_WIFI in WiFi_Rev.1.PcbDoc <=> Net PA4_GPIO6_WIFI in WiFi_Rev.2.PcbDoc
 - Net PC2_WF_RESET in WiFi_Rev.1.PcbDoc <=> Net PC2_WF_RESET in WiFi_Rev.2.PcbDoc
 - Net PC15_SPW_BOOT0 in WiFi_Rev.1.PcbDoc <=> Net PC15_SPW_BOOT0 in WiFi_Rev.2.PcbDoc

배선 수정

Projects Navigator PCB Differences PCB Filter View Configuration PCB ActiveRoute
X:11.05mm Y:23.225mm Grid:0.025mm (Hotspot Snap (All Layers))
Idle state - ready for command

설계파일 차이점 비교

Altium Designer (19.0.15) interface showing a comparison between two PCB design files: WiFi_Rev.1.PcbDoc and WiFi_Rev.2.PcbDoc.

The Differences panel on the left lists 30 detected differences. The highlighted difference is:

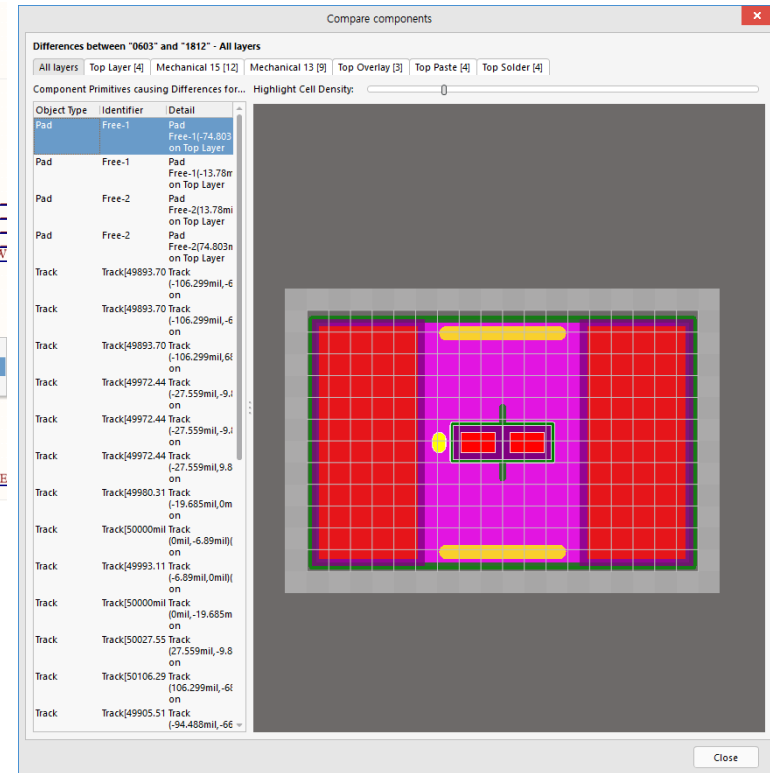
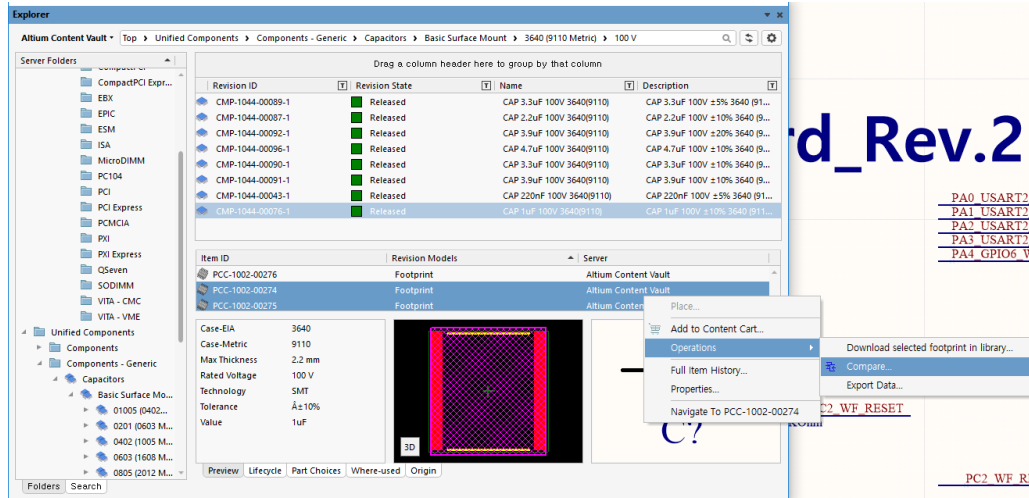
- Net PC2_WF_RESET in WiFi_Rev.1.PcbDoc <=> Net PC2_WF_RESET in WiFi_Rev.2.PcbDoc

The main workspace displays the PCB layout with a blue trace highlighted in orange, indicating the pattern width modification. A white box with an orange border contains the text "패턴 폭 수정" (Pattern Width Modification).

2. Footprint 차이점 비교

Footprint 차이점 비교

- Explorer 패널에서는 Altium Content Vault와 Nexus Server에 등록된 사용자 정의 라이브러리를 사용할 수 있다. 여기에서 비교하고자 하는 Footprint를 두개 선택 → 마우스 오른쪽 버튼 클릭 → Operations → Compare... 메뉴를 실행하면 두개의 부품을 비교할 수 있다.
 - Altium Content Vault : Altium에서 기본으로 제공
 - Altium Nexus Server : 사용자가 생성한 라이브러리 (Nexus Server를 별도로 구축해야 하며, Altium Designer 단독으로 는 사용 불가)





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Template Visual Guide, version 1.0

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